

Improving WCET Precision for Synthesized Code from Simulink/Stateflow

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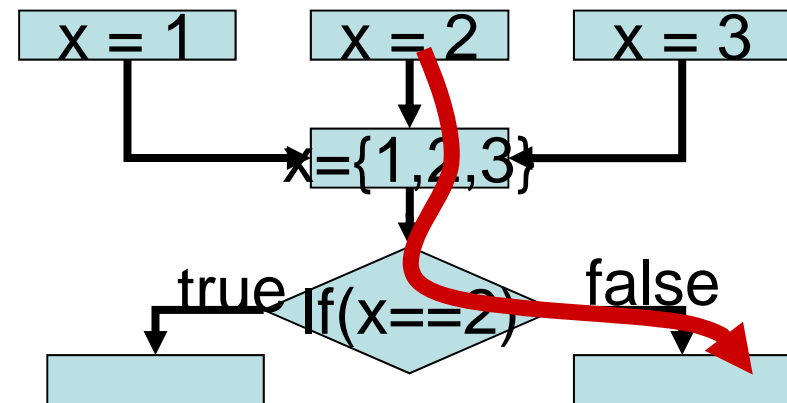
WCET: Worst Case Execution Time

- **WCET bound** required for scheduling
 - **Safe:** safety-critical embedded systems
 - **Precise:** recourses optimization
- **WCET** is generally not known

Static Timing Analysis

- Static timing analysis computes superset of execution paths

→ Safe guarantee in
WCET bound

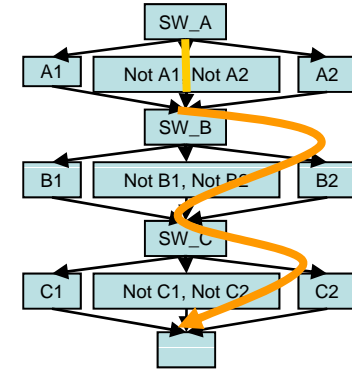
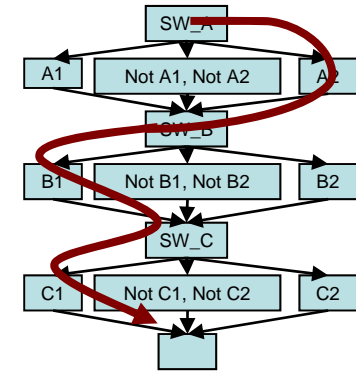
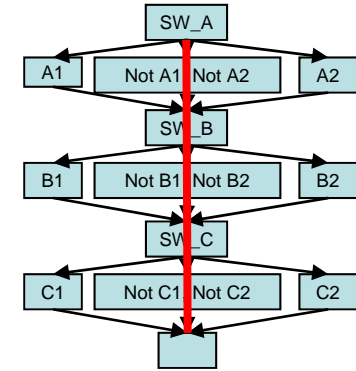
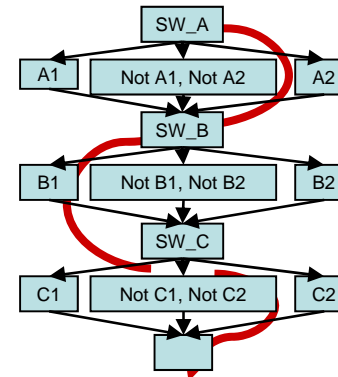
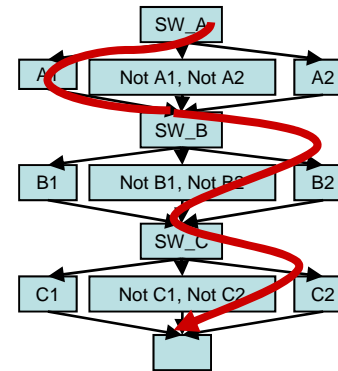
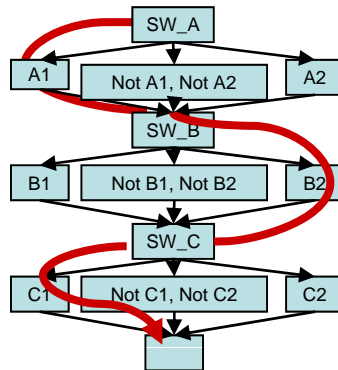
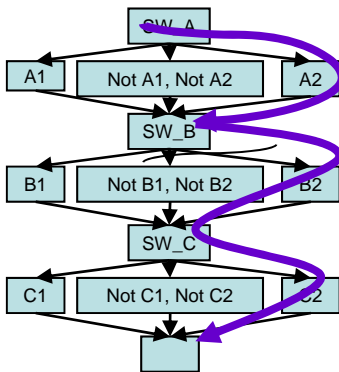
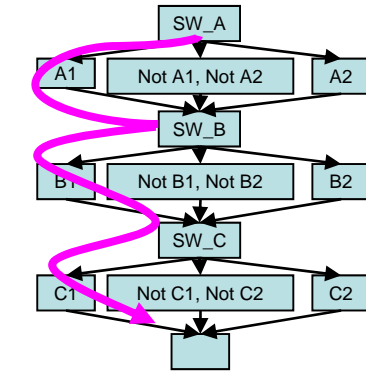


- Potentially **overestimation**
 - relations between conditions are often not tracked precisely
 - loses context information at control flow joins
 - Context ^context ^context...
- Lots of infeasible paths

The problem

Feasible / Infeasible paths ?

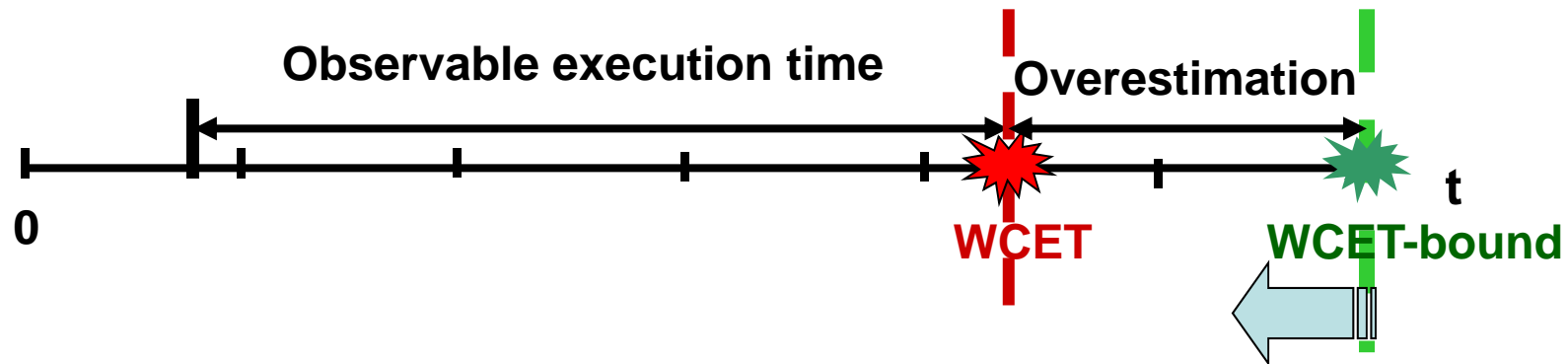
→ Potentially high overestimation



... and more ...

Motivation

- Improve precision of static timing analysis

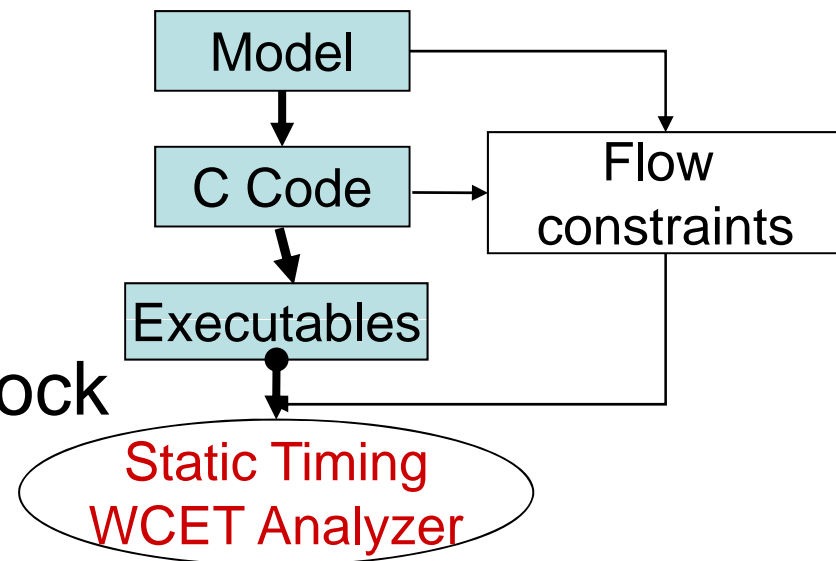


- Matlab Simulink/Stateflow widely used in industry
- Leverage context information from model
→ track execution context precisely

Ideas:

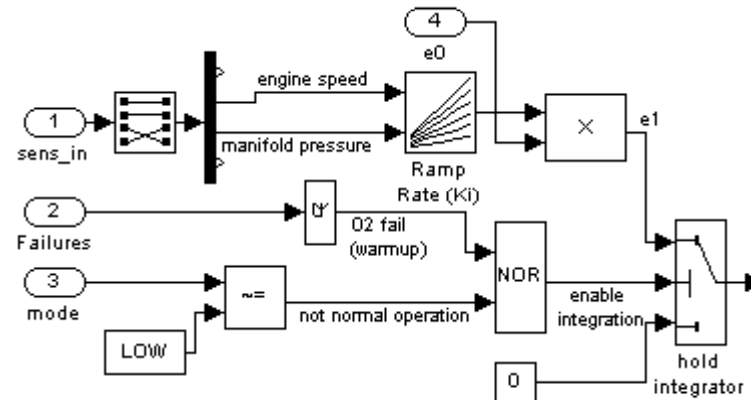
- Relations between conditions are often obvious in models and C code
- Deriving dependency information as flow constraints as input for WCET static analysis tool

- From single block
- To correlated blocks
- To clustered sets of block



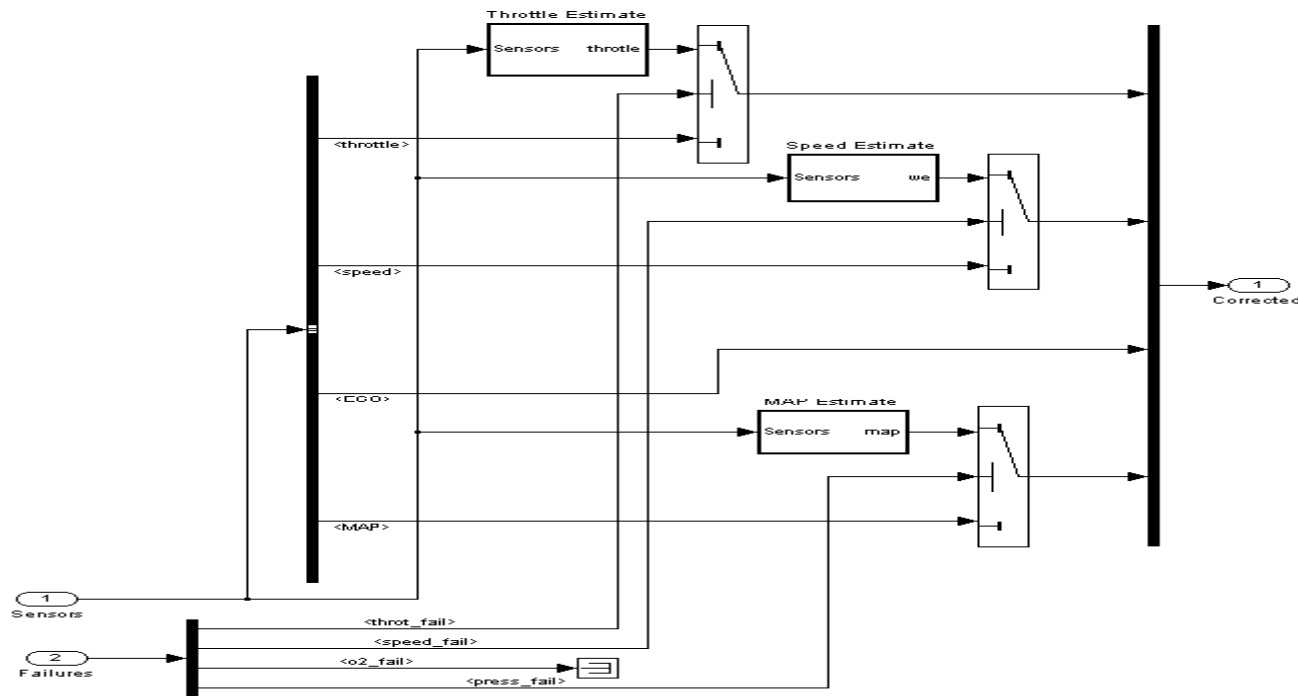
Approaches

- Single block: Use-Define



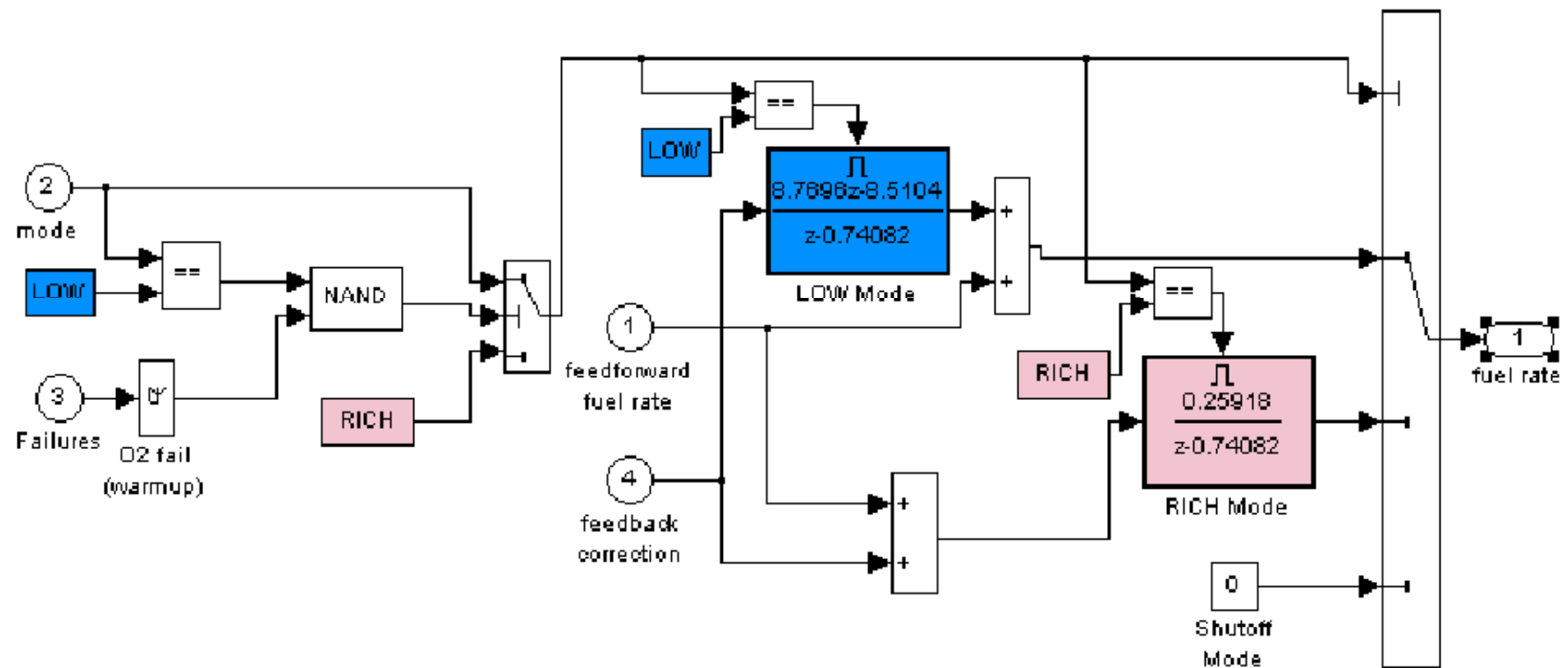
Approaches (cont.)

- Cross blocks: Correlation relations



Approaches (cont.)

- Clustered sets of blocks:
 - Mode-dependent execution context



Evaluation setting

- Tool: aiT WCET Analyzer
 - Analysis executables as input
 - Used in aerospace industry, especially Airbus French
 - Extended to users inclusive automotive domain
 - Successes in the WCET Tool Challenge 2006

Evaluation setting (cont.)

- Benchmark from Matlab : Fuel-rate control system
 - Automotive application
 - Typical PID control with reconfigurable control logic for operating modes
 - Size:

Model (.mdl) size: **196 KB**

Generated C and h files

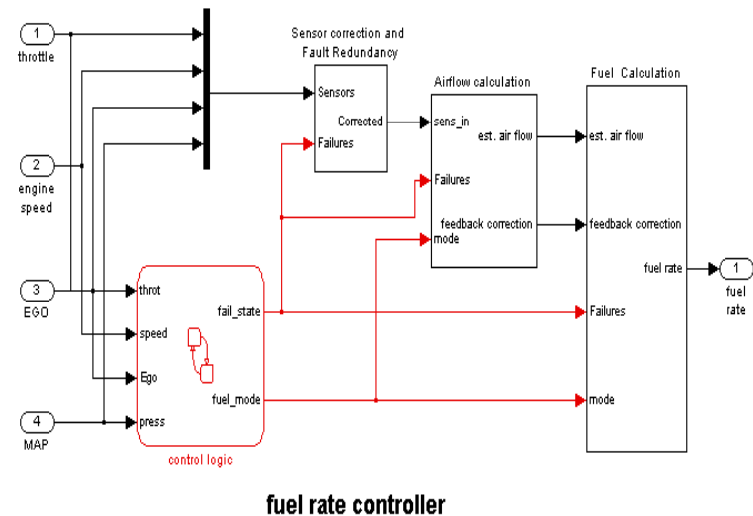
File count: **12**

File size: **116K**

K-LOC rating: **2.762**

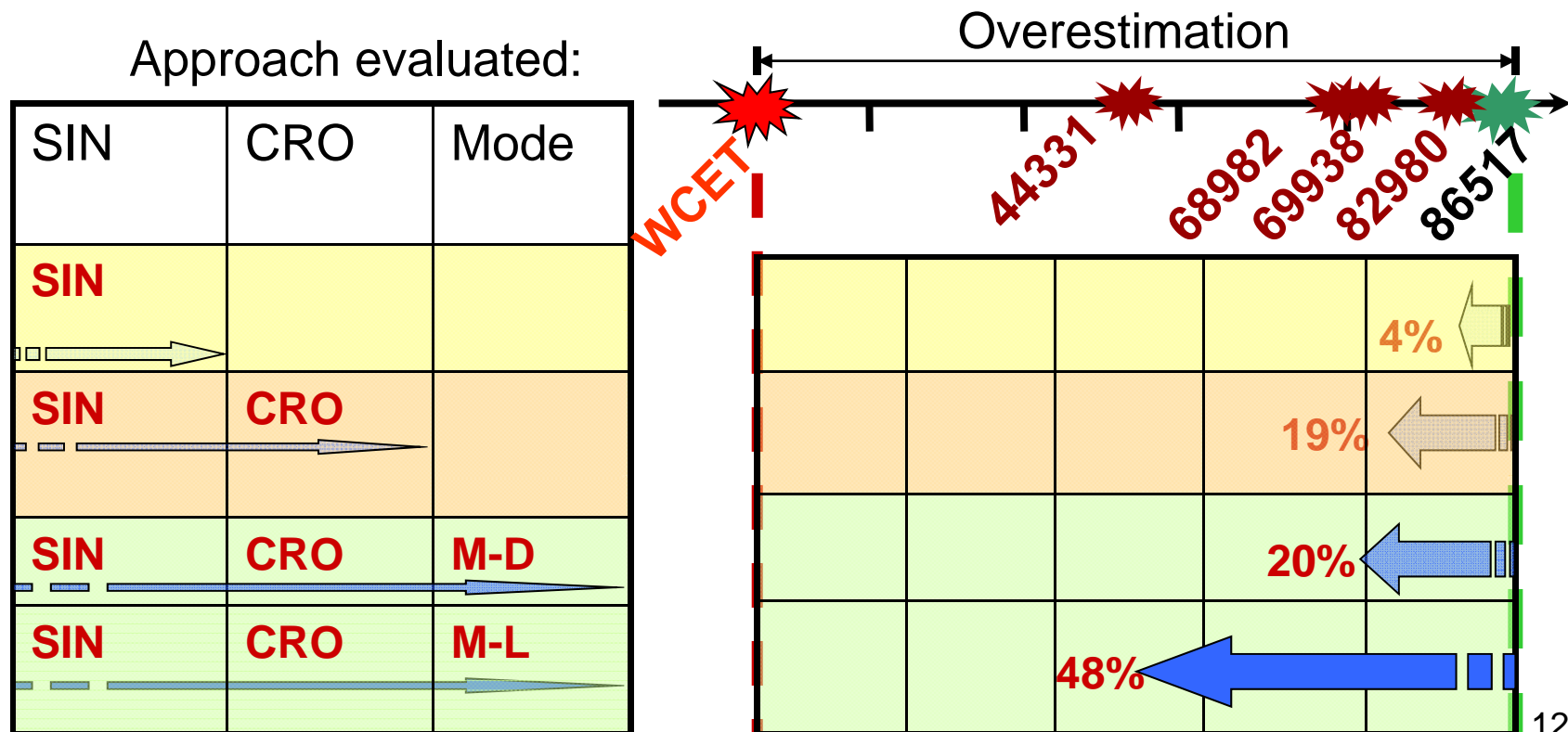
Average K-LOC: **0.23**

Executables (.elf) size: **37 KB** TriCore 1769B



Experimental results

- Precision improvement: 19%
 - More precise in mode-specified execution context, depending on scheduling requirement



Conclusion

- Developed concepts and methods to improve WCET precision by deriving flow constraints from models.
- Evaluated the proposed methods with industrial WCET tool and benchmark of Matlab:
 - Initial results is promising in precision improvement
- Future work
 - Apply to real industrial application
 - Extend to automatically implementation
 - Flow constraints generation and transformation from .mdl

References

1. Thesing, S., Souyris, J., Heckmann, R., Randimbivololona, F., Langenbach, M., Wilhelm, R., Ferdinand, C.: An Abstract Interpretation-Based Timing Validation of Hard Real-Time Avionics Software Systems. In: Proceedings of DSN. (2003)
2. Ferdinand, C., Heckmann, R., Langenbach, M., Martin, F., Schmidt, M., Theiling H., Thesing, S., Wilhelm, R.: Reliable and precise WCET determination for a real-life processor. In: EMSOFT. Volume 2211 of LNCS. (2001) 469–485
3. Cousot, P., Cousot, R.: Abstract Interpretation: A Unified Lattice Model for Static Analysis of Programs by Construction or Approximation of Fixpoints. In: POPL77, Los Angeles, California (1977) 238–252
4. Heckmann, R., Langenbach, M., Thesing, S., Wilhelm, R.: The influence of processor architecture on the design and the results of WCET tools. Proceedings of the IEEE **91** (2003) 1038–1054
5. AbsInt Angewandte Informatik GmbH: aiT WCET Analyzer. <http://www.absint.com/>
6. Stein, I., Martin, F.: Analysis of path exclusion at the machine code level. In: Proceedings of WCET. (2007)
7. Ju, L., Huynh, B.K., Roychoudhury, A., Chakraborty, S.: Performance debugging of Esterel specifications. In: CODES+ISSS. (2008) 173–178
8. Kirner, R., Lang, R., Freiberger, G., Puschner, P.: Fully automatic worst-case execution time analysis for Matlab/Simulink models. In: ECRTS. (2002) 31–40
9. Tan, L.: The worst-case execution time tool challenge 2006. International Journal on Software Tools for Technology Transfer (STTT) **11** (2009) 133 – 152
10. Ferdinand, C., Heckmann, R., Wolff, H.J., Renz, C., Parshin, O., Wilhelm, R.: Towards model-driven development of hard real-time systems. In: Proceedings of ASWSD. (2006) 145–160
11. Ferdinand, C., Heckmann, R., Sergent, T.L., Lopes, D., Martin, B., Fornari, X., Martin, F.: Combining a high-level design tool for safety-critical systems with a tool for WCET analysis on executables. In: Proceedings of ERTS. (2008)
12. Mathworks: Matlab Simulink/Stateflow, <http://www.mathworks.com>

Thank you!

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