14th International Conference on
RELIABLE SOFTWARE TECHNOLOGIES
ADA-EUROPE 2009

BREST, FRANCE, 8-12 JUNE 2009

FINAL PROGRAM

http://www.ada-europe.org/conference2009.html
The 14th International Conference on Reliable Software Technologies – Ada-Europe 2009 will take place in Brest, France, on 8-12 June 2009. The conference has established itself as an international forum for providers, practitioners and researchers into reliable software technologies. Following tradition, the conference will span a full week, with at its centre from Tuesday to Thursday a three-day technical program accompanied by vendor exhibitions, and at either end on Monday and Friday a string of parallel tutorials and workshops. Brest enjoys a wonderful setting, on the shores of a very large bay, with inland beauty and coastal Brittany surrounding it. The city is a centre of commerce, combining the cobblestone streets and fortifications of the old port with all the attractions and facilities of a modern city.

**Overview of the Week**

<table>
<thead>
<tr>
<th>Monday 8 June</th>
<th>Morning</th>
<th>Late Morning</th>
<th>Early Afternoon</th>
<th>Afternoon</th>
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<tbody>
<tr>
<td>Tutorials and Workshop</td>
<td>Joyce Tokar</td>
<td>Workshop on Software Vulnerabilities</td>
<td>Quentin Ochem</td>
<td>Building Cross Language Applications using Ada</td>
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<td></td>
<td>John Mc Cormick</td>
<td>An Introduction to Parallel and Real-Time Programming with Ada</td>
<td>Pat Rogers</td>
<td>Software Fault Tolerance</td>
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<td></td>
<td>William Bail</td>
<td>Software Measures for Building Dependable Software Systems</td>
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<thead>
<tr>
<th>Tuesday 9 June</th>
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<tbody>
<tr>
<td>Sessions &amp; Exhibition</td>
<td>Keynote Talk</td>
<td>John Benito</td>
<td>High Integrity</td>
<td>Testing</td>
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<td></td>
<td>JTC 1/SC 22/WG 23 Work on Programming Language Vulnerabilities</td>
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<td>Education</td>
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<td></td>
<td>Vendor session</td>
<td>Model-Driven Engineering</td>
<td>Industrial Presentations</td>
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<td></td>
<td>Keynote Talk</td>
<td>Pierre Sens</td>
<td>Real Time</td>
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<td>Fault Tolerance in Large Scale Distributed Systems</td>
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<th>Wednesday 10 June</th>
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<td>Vendor session</td>
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<td>Ensuring Software Integrity</td>
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<td></td>
<td>Keynote Talk</td>
<td>Peter H. Feiler</td>
<td>Model-Driven Engineering and AADL</td>
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<td>Validation of Safety-Critical Systems with AADL</td>
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<th>Thursday 11 June</th>
<th>Morning</th>
<th>Late Morning</th>
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<tr>
<th>Friday 12 June</th>
<th>Morning</th>
<th>Late Morning</th>
<th>Early Afternoon</th>
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<tbody>
<tr>
<td>Tutorials and Workshop</td>
<td>Bruce Lewis</td>
<td>Workshop on AADL (Architecture Analysis and Design Language)</td>
<td>Roderick Chapman</td>
<td>SPARK - The Libre Language and Toolset for High-Assurance Software</td>
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<td></td>
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<td>Rob Pettit</td>
<td>Designing Real-Time, Concurrent, and Embedded Software Systems using UML and Ada</td>
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<td></td>
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<td></td>
<td>Ian Broster</td>
<td>Execution Time: Analysis, Verification, and Optimization in Reliable Systems</td>
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| | Pat Rogers | Hard Real-Time and Embedded Systems Programming | | |
| | | | | |
## Conference Schedule

<table>
<thead>
<tr>
<th>Tuesday 9 June</th>
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<tbody>
<tr>
<td><strong>8:30 – 9:00</strong></td>
<td><strong>Keynote Talk</strong></td>
<td><strong>Keynote Talk</strong></td>
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<tr>
<td>Welcome and Opening Address</td>
<td>Pierre Sens</td>
<td>Peter H. Feiler</td>
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<td></td>
<td>Chair: J. A. de la Puente</td>
<td>Chair: F. Singhoff</td>
</tr>
<tr>
<td><strong>9:00 – 9:30</strong></td>
<td><strong>Keynote Talk</strong></td>
<td><strong>Validation of Safety-Critical Systems with AADL</strong></td>
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<tr>
<td>Keynote Talk</td>
<td>John Benito</td>
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<td>Chair: F. Kordon</td>
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<tr>
<td><strong>9:30 – 10:00</strong></td>
<td><strong>Coffee &amp; Exhibition</strong></td>
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<tr>
<td><strong>JTC 1/SC 22/WG 23 work on Programming Language Vulnerabilities</strong></td>
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<tr>
<td><strong>10:00 – 10:30</strong></td>
<td><strong>Real Time</strong></td>
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<td><strong>10:30 – 11:00</strong></td>
<td><strong>High Integrity</strong></td>
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<tr>
<td><strong>11:00 – 12:30</strong></td>
<td>Real Time</td>
<td>Model-Driven Engineering and AADL</td>
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<tr>
<td><strong>12:30 – 14:00</strong></td>
<td>Lunch &amp; Exhibition</td>
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<tr>
<td><strong>10:00 – 10:30</strong></td>
<td>Coffee &amp; Exhibition</td>
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<td><strong>11:00 – 12:30</strong></td>
<td>High Integrity</td>
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<tr>
<td>Requirements on a Target Programming Language suited for a High-Integrity MDE Environment, Alessandro Zovi and Tullio Vardanega</td>
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<tr>
<td>A restricted middleware profile for high-integrity distributed real-time systems, Santiago Uruéna, Juan Zamorano and Juan Antonio de la Puente</td>
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<tr>
<td>Validating safety and security requirements for partitioned architectures, Julien Delange, Laurent Pautet and Peter Feiler</td>
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<tr>
<td>Implementation of the Ada 2005 Task Dispatching Model in MaRTE OS and GNAT, Mario Aldea Rivas, Michael González Harbour and José F. Ruiz</td>
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<tr>
<td>Combining EDF and FP: Analysis and Implementation in Ada 2005, Alan Burns, Andy Wellings and Fengxiang Zhang</td>
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<tr>
<td>Predicated Worst-Case Execution-Time Analysis, Amine Marref and Guillem Bernat</td>
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<tr>
<td>Modeling AADL Data Communication with BIP, Lei Pi, Jean-Paul Bodeveix and Mamoun Filali-Amine</td>
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<tr>
<td>Formal Verification of AADL Specifications in the Topcased Environment, François Vernadat, Bernard Berthomieu, Jean-Paul Bodeveix, Christelle Chaudet, Silvano Dal Zilio and Mamoun Filali</td>
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<tr>
<td>Process-Algebraic Interpretation of AADL Models, Oleg Sokolsky, Insup Lee and Duncan Clarke</td>
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<tr>
<td>OCARINA: An Environment for AADL Models Analysis and Automatic Code Generation for High Integrity Applications, Lasnier Gilles, Zaïlila Bechir, Pautet Laurent and Hugues Jérôme</td>
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Unless otherwise indicated, the Ada-Europe sessions will take place in the “Grand Amphithéâtre” (Main amphitheatre) room. Coffee breaks and exhibition will take place in the main lobby called “Centre vie”.
<table>
<thead>
<tr>
<th>Time</th>
<th>Tuesday 9 June</th>
<th>Wednesday 10 June</th>
<th>Thursday 11 June</th>
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</thead>
</table>
| 14:00 - 15:00| **Testing**  
Chair : J.P. Rosen  
On Comparing Testing Criteria for Logical Decisions, Man Fai Lau and Yuen Tak Yu  
Model Checking Techniques for Test Generation from Business Process Models, Didier Buchs, Levi Lucio and Ang Chen | **Model-Driven Engineering**  
Chair : J. Hugues  
Implementing Reactive Systems with UML State Machines and Ada 2005, Sergio Sánchez, Silvia Terrasa, Vicent Lorente and Alfons Crespo  
Modelling and Evaluating Real-Time Software Architectures, Jose L. Fernandez and Gloria Marmol  
A Formal Foundation for Metamodelling, Liliana Favre | **Industrial Presentations**  
Chair : B. Lewis  
AADL Experimentation at Airbus, Pierre Gaufillet, Sébastien Heim, Hugues Bonnin and Pierre Dissaux  
Generating Component-Based AADL Applications with MyCCM-HI and Ocinar, Thomas Vergnaud, Gregory Haik and Jérôme Hugues  
Flight Management System validation thought performance analysis and simulation, Véronique Fabre, Catherine Teseidre and Madeleine Faugère |
| 15:00 - 15:30| **Coffee & Exhibition**  
Chair : J. McCormick  
An Experience on Ada Programming Using Online Judging, Francisco J. Montoya-Dato, Jose Luis Fernández-Alemán and Ginés García-Mateos  
Using Java or C# Monitor for a concurrency kernel implies defensive multithreading programming, Claude Kaiser and Jean-François Prudat-Peyre. | **Coffee & Exhibition**  
Chair : P. Dissaux  
“Petit amphithéâtre” room  
Region-Based Memory Management for Safety-Critical Systems, Tucker Taft  
Pattern-Based Refactoring Shrinks Maintenance Costs, John S. Harbaugh  
Couverture - Project Coverage - An Innovative Open Framework for Coverage Analysis of Safety Critical Applications, Matteo Bordin | **Coffee break**  
Chair : O. Sokolsky  
Conceptual Modeling for System Requirements Enhancement, Eric Le Pors and Olivier Grisvard  
Coloured Petri nets for chronicle recognition, Christine Choppy, Olivier Bertrand and Patrice Carle |
| 15:30 - 16:00|  | **GAP meeting**  
**Industrial Presentations**  
Chair : R. Chapman  
Region-Based Memory Management for Safety-Critical Systems, Tucker Taft  
Pattern-Based Refactoring Shrinks Maintenance Costs, John S. Harbaugh  
Couverture - Project Coverage - An Innovative Open Framework for Coverage Analysis of Safety Critical Applications, Matteo Bordin |  |
| 16:00 - 16:30| **Education**  
Chair : J. McCormick  
An Experience on Ada Programming Using Online Judging, Francisco J. Montoya-Dato, Jose Luis Fernández-Alemán and Ginés García-Mateos  
Using Java or C# Monitor for a concurrency kernel implies defensive multithreading programming, Claude Kaiser and Jean-François Prudat-Peyre. | **Vendor session**  
Chair : P. Dissaux  
“Petit amphithéâtre” room  
Region-Based Memory Management for Safety-Critical Systems, Tucker Taft  
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Couverture - Project Coverage - An Innovative Open Framework for Coverage Analysis of Safety Critical Applications, Matteo Bordin |  |
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Couverture - Project Coverage - An Innovative Open Framework for Coverage Analysis of Safety Critical Applications, Matteo Bordin |  |
| 17:00 – 17:30| **Welcome Reception** |  | **Closing Address and Best-Presentation Award** |
| 17:30 - 18:00|  |  |  |
| 18:00 - 19:00|  |  |  |
| From 19:00  |  |  |  |

An informal GNAT Academic Program (GAP) meeting will be held during the afternoon Wednesday break (15h30-16h30) at “Grand Amphithéâtre” “(Main amphitheatre) room to update members on the latest news and exchange views and ideas on the future development of the program. All Ada academics and students are welcome.
**INVITED SPEAKERS**

Three eminent keynote speakers have been selected to open each day of the core conference program:

- **John Benito** (Blue Pilot Consulting, USA), a leading member of the international standardization and programming languages community, will deliver a talk entitled “ISO JTC 1/SC 22/WG 23 Work on Programming Language Vulnerabilities”.

- **Pierre Sens** (LIP6, Université Pierre et Marie Curie, Paris), a researcher in software technologies for distributed systems will discuss fault tolerance technologies in a talk entitled “Fault Tolerance in Large Scale Distributed Systems”.

- **Peter H. Feiler** (CMU/SEI USA), a worldwide expert in architecture modeling and verification, will elaborate on “Validation of Safety-Critical Systems with AADL”.

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**WORKSHOP SCHEDULE**

<table>
<thead>
<tr>
<th>Monday 8 June</th>
<th>Full day</th>
<th>Joyce Tokar (Pyrrhus Software, USA)</th>
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<tr>
<td></td>
<td>B3-044 room</td>
<td><em>Workshop on Software Vulnerabilities</em></td>
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<tr>
<td>Friday 12 June</td>
<td>Full day</td>
<td>Bruce Lewis (US Army, USA)</td>
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<td></td>
<td>B1-010 room</td>
<td><em>Workshop on AADL</em></td>
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**Detailed schedule of Workshop on Vulnerabilities**

Given the large focus on software vulnerabilities in the current market place, ISO has instantiated the “Other Working Group: Vulnerabilities” as ISO WG23. ISO WG23 has submitted a Technical Report that captures the current view of software vulnerabilities. This workshop will focus on the content of this Technical Report and its applicability to Ada and SPARK.

The objective of the workshop is to define the Ada Annex to the ISO WG23 Technical Report on Vulnerabilities, and to identify breaks and gaps in this Technical Report with respect to the programming language Ada.

- 09:00 – 10:30 Overview and Update of the ISO WG23 Technical Report on Vulnerabilities. J. Benito/E. Ploedereder
  Expunging the C/C++ Bias. J.P. Rosen
  Ada and Programming Language Vulnerabilities. S. Michell

- 10:30 – 11:00 Coffee Break

- 11:00 – 12:30 Ada and Programming Language Vulnerabilities (cont). S. Michell
  Argument for Language Subsetting. T. Vardanega.

- 12:30 – 14:00 Lunch
The AADL workshop will propose a session that emphasizes learning AADL concepts. Then a series of tool demonstrations is proposed to help you applying these concepts to industrial case studies.

The AADL provides a means to precisely describe the hardware/software architecture to support quantitative model-based architectural engineering early and throughout the lifecycle and well as efficient, automated system integration. Through a standardized definition of components and their interaction it supports integration of architectural models across system developers as well as standard-based analysis tools.

Two modeling approaches are demonstrated. The first is to directly model in the AADL language with analysis and generation to the AADL specifications. The second involves the use of high-level architectural modeling tools that allow one to capture the system design in a domain specific way, and then map it to AADL to perform analysis and generation, thus lowering the AADL learning curve.
# Tutorial Schedule

| Tutorial | Monday 8 June | T1 | Full day B3-042 room | Quentin Ochem (AdaCore, France)  
*Building Cross Language Applications using Ada* |
|----------|----------------|----|----------------------|----------------------------------------------------------------------------------|
| T2       | Morning B3-046 room | John Mc Cormick (University of Northern Iowa, USA)  
*An Introduction to Parallel and Real-Time Programming with Ada* |
| T3       | Afternoon B3-046 room | Pat Rogers (AdaCore, USA)  
*Software Fault Tolerance* |
| T4       | Morning B1-026 room | William Bail (MITRE, USA)  
*Software Measures for Building Dependable Software Systems* |
| T5       | Full day B3-044 room | Roderick Chapman (Praxis High Integrity Systems, UK)  
*SPARK - The Libre Language and Toolset for High-Assurance Software* |
| T6       | Morning B3-042 room | Pat Rogers (AdaCore, USA)  
*Hard Real-Time and Embedded Systems Programming* |
| T7       | Afternoon B3-046 room | Rob Pettit (The Aerospace Corporation, USA)  
*Designing Real-Time, Concurrent, and Embedded Software Systems using UML and Ada* |
| T9       | Afternoon B1-026 room | Ian Broster (Rapita Systems, UK)  
*Execution Time: Analysis, Verification, and Optimization in Reliable System* |

Morning tutorial sessions will start at 9:00 and end at 12:30. Afternoon sessions will start at 14:00 and end at 17:30. Coffee breaks will be at 10:30 - 11:00 and at 15:30 - 16:00.

# Working Group Schedule

<table>
<thead>
<tr>
<th>Working Group</th>
<th>Friday 12 June</th>
<th>Morning Start at 9:00 B1-006 room</th>
<th>Meeting #56 of ISO/IEC JTC1/SC22/WG9</th>
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<tr>
<td></td>
<td>Afternoon Start at 14:00 B1-006 room</td>
<td>ARG Meeting</td>
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LIST OF EXHIBITORS

The exhibition will open at the morning break on Tuesday and run continuously until the end of the afternoon break on Thursday. It takes place in the heart of the conference centre just next to the meeting halls. The coffee breaks are held in the exhibition area. The breaks on Tuesday to Thursday span one hour so as to allow the attendees comfortable time to visit the exhibition. Exhibitors will also deliver a presentation in the vendor sessions on Tuesday and Wednesday (see vendor sessions schedule below). Exhibitors are AdaCore, Aonix, Rapita Systems, Ellidiss Software and IBM.

VENDOR SESSIONS SCHEDULE

“Petit amphitheatre” (small amphitheatre)

<table>
<thead>
<tr>
<th></th>
<th>Time</th>
<th>Vendor</th>
<th>Presentation Title and Details</th>
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<tbody>
<tr>
<td><strong>Tuesday 9 June</strong></td>
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<tr>
<td>16:00</td>
<td>Rapita Systems</td>
<td>A. Coombes</td>
<td>Worst Case Execution Time of Ada Applications: What, Why and How?</td>
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<tr>
<td>16:30</td>
<td>Ellidiss Software</td>
<td>T. Elliston</td>
<td>Ellidiss tools for Ada and AADL</td>
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<tr>
<td><strong>Wednesday 10 June</strong></td>
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<tr>
<td>14:00</td>
<td>IBM Rational</td>
<td>P. Leblanc</td>
<td>Ada Development with UML and the Telelogic Rhapsody Product</td>
</tr>
<tr>
<td>14:30</td>
<td>AdaCore</td>
<td>R. Friess</td>
<td>An Open Source for Innovation</td>
</tr>
<tr>
<td>15:00</td>
<td>Aonix</td>
<td>T. Grosman</td>
<td>Ada and Java: The Aonix Perspective</td>
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MAP OF THE CONFERENCE CENTER
SOCIAL PROGRAM

Welcome Reception

The social program of the conference will open with a welcome reception at Oceanopolis: Brittany’s sea park by the Marina in Brest. The Sea Park is organized around three buildings which highlight the diversity and the habitat of the seas around the world: a temperate building which presents sea life around Brittany, the tropical building displays tropical and colorful fishes and sharks, the polar building is home to about 40 penguins. The 50 aquariums make it possible to observe the various animals and plants in their habitat. **Ellidiss Technologies sponsors the reception at Oceanopolis.**

Schedule of the social event:
- 17:15 bus transfer from conference site to Oceanopolis
- 18:00–20:00 visit of the polar and the tropical aquariums
- 20:15–21h30: concert from the University of Brest orchestra
- 21:00–23:00 cocktail dinner at the Britain building.
- 23:00: bus transfer to hotels (both to IGESA, Hotel Agena, Hotel Belvédère and to Télécom Bretagne)

Conference Banquet

The conference banquet will take place by the sea side, in the charming village of Porspoder, located 25 km northwest of Brest; this area provides a spectacular landscape of rough cliffs and sandy coves, and impressive marine streams due to the collision of the Channel and the Atlantic waters. The banquet will take place in the restaurant called “Le Chenal”. **AdaCore sponsors the banquet.**

Schedule of the social event:
- 19:00: bus transfer from conference site to Porspoder/Le Chenal
- 20:00-23:00: banquet
- 23:00: bus transfer to hotels (both to IGESA, Hotel Agena, Hotel Belvédère and to Télécom Bretagne)

La Pointe du Raz (a famous rocky peak)
PROGRAM COMMITTEE

Alejandro Alonso, Universidad Politécnica de Madrid, Spain
Johann Blieberger, Technische Universität Wien, Austria
Maarten Boasson, University of Amsterdam, The Netherlands
Bernd Burgstaller, Yonsei University, Korea
Dirk Craeynest, Aubay Belgium & K.U.Leuven, Belgium
Alfons Crespo, Universidad Politécnica de Valencia, Spain
Juan A. de la Puente, Universidad Politécnica de Madrid, Spain
Raymond Devillers, Université Libre de Bruxelles, Belgium
Philippe Dhaussy, ENSIETA/LISyC, France
Michael González Harbour, Universidad de Cantabria, Spain
José-Javier Gutiérrez, Universidad de Cantabria, Spain
Andrew Hately, Eurocontrol CRDS, Hungary
Günter Hommel, Technische Universität Berlin, Germany
Jérôme Hugues, Telecom Paris, France
Hubert Keller, Institut für Angewandte Informatik, Germany
Yvon Kermarrec, Télécom Bretagne, France
Fabrice Kordon, Université Pierre & Marie Curie, France
Albert Llemosí, Universitat de les Illes Balears, Spain
Franco Mazzanti, ISTI-CNR Pisa, Italy
John McCormick, University of Northern Iowa, USA
Stephan Michell, Maurya Software, Canada
Javier Miranda, Universidad Las Palmas de Gran Canaria, Spain
Daniel Moldt, University of Hamburg, Germany
Scott Moody, Boeing, USA
Laurent Pautet, Telecom Paris, France
Laure Petrucci, LIPN, Université Paris 13, France
Luís Miguel Pinho, Polytechnic Institute of Porto, Portugal
Erhard Plödereder, Universität Stuttgart, Germany
Jorge Real, Universidad Politécnica de Valencia, Spain
Alexander Romanovsky, University of Newcastle upon Tyne, UK
Jean-Pierre Rosen, Adalog, France
Lionel Seinturier, Université de Lille, France
Frank Singhoff, UBO/LISyC, France
Oleg Sokolsky, University of Pennsylvania, USA
Ricky Sward, MITRE, USA
Tullio Vardanega, Università di Padova, Italy
François Vernadat, LAAS-CNRS, Université de Toulouse, Insa
Andy Wellings, University of York, UK
Jürgen Winkler, Friedrich-Schiller Universität, Germany
Luigi Zaffalon, University of Applied Sciences, W. Switzerland

INDUSTRIAL COMMITTEE

Guillem Bernat, Rapita Systems, UK
Agusti Canals, CS, France
Roderick Chapman, Praxis HIS, UK
Colin Coates, Telelogic, UK
Dirk Craeynest, Aubay Belgium & K.U.Leuven, Belgium
Tony Elliston, Ellidiss Software, UK
Franco Gasperoni, AdaCore, France
Hubert Keller, Forschungszentrum Karlsruhe GmbH, Germany
Bruce Lewis, US Army, USA
Ahlan Marriott, White-Elephant GmbH, Switzerland
Rei Stråhle, Saab Systems, Sweden

FURTHER INFORMATION

The conference web site at http://www.ada-europe.org/conference2009.html gives full and up to date details of the program. The web site also provides details of the venue, including travel advice, instructions to reserve hotel accommodation in the conference venue, maps and a list of hotels close by.

Exhibiting and Sponsoring details are also on the web site; a sliding scale of sponsorship provides a range of benefits. All levels include display of your logo on the conference web site and in the program. The lowest level of support is very affordable.

Pointe Saint Mathieu (lighthouse with a signal station and abbey ruins)
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